Amendments to the Specification:

Please amend the Title as follows:

MEMORY CONTROLLER ~~WITH 1X/MX WRITE CAPABILITY~~<u>DRIVER CIRCUITRY</u>
<u>HAVING A MULTIPLEXING STAGE TO PROVIDE DATA TO AT LEAST N-1 OF N</u>
<u>DATA PROPAGATION CIRCUITS, AND HAVING OUTPUT MERGING CIRCUITRY</u>
<u>TO ALTERNATELY COUPLE THE N DATA PROPAGATION CIRCUITS TO A DATA</u>
<u>PAD TO GENERATE EITHER A 1X OR MX STREAM OF DATA</u>

Please replace paragraph [0001] with the following amended paragraph:

[0001]   This is a continuation of copending<u> U.S. patent</u> application number
09/827,768 filed on April 7, 2001,<u> now U.S. Patent 6,678,811 issued January 13,</u>
<u>2004,</u> which is hereby incorporated by reference herein.